

BIODATA

Name:- Dheeraj Singh Rajput
Date of Birth:- 04-05-1990
Designation:- Assistant Professor
Date of joining:- 11-07-2016
Total Experience of Teaching and Research:- 2.5 Year
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Qualifications:-

Name of course	Institute/University	Year of Passing
B.E./B.TECH	MITS GWALIOR	2011
M.E./M.TECH (VLSI DESIGN & EMBEDDED SYSTEM)	DELHI TECHNOLOGICAL UNIVERSITY DELHI	2014
Ph D (Title)		
Research Interests:-	LOW POWER VLSI ,DIGITAL & ANALOG CIRCUIT	

List of Publications:

1. Research papers presented in Conference/Seminar/Workshop.

S.No.	Title of the Paper	Name of the Organizing Institute	Name of the Program	Date
1.	Timer circuit using outran	IEEE CONFERENCE	IEEE CONFERENCE	26-27/FEB/2015
2.	Self-Assessing process in HPC Application:	IEEE CONFERENCE	IEEE CONFERENCE	21-23/MARCH/2017
	Alternate approach to Master/Slave Processes			

Academic/Administrative Responsibilities held at Institute / State level:-

S.No.	Responsibilities held	Duration
1.	Website & Automation	
2.	Pradhanmatri Kausal Vikash Yojana	
3.	Faculty adviser of IT Department	